

Refine Search

Search Results -

Terms	Documents
L1 and multiplex\$4 and neural near network	34

Database:

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US Patents Full-Text Database
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JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

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Search History

DATE: Thursday, April 21, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

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result set

DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR

L2 L1 and multiplex\$4 and neural near network

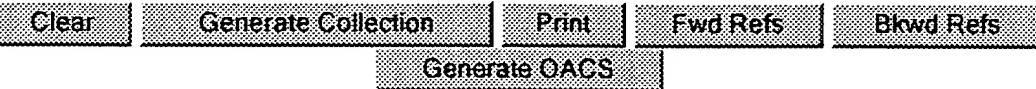
34 L2

L1 706/15.ccls.

316 L1

END OF SEARCH HISTORY

Hit List



Search Results - Record(s) 1 through 34 of 34 returned.

1. Document ID: US 20040122783 A1

Using default format because multiple data bases are involved.

L2: Entry 1 of 34

File: PGPB

Jun 24, 2004

PGPUB-DOCUMENT-NUMBER: 20040122783

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040122783 A1

TITLE: String search neuron for artificial neural networks

PUBLICATION-DATE: June 24, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Specht, Donald F	Los Altos	CA	US	
Paillet, Guy	Corte Madera	CA	US	

US-CL-CURRENT: 706/6; 706/15

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawings](#)

2. Document ID: US 20030140023 A1

L2: Entry 2 of 34

File: PGPB

Jul 24, 2003

PGPUB-DOCUMENT-NUMBER: 20030140023

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030140023 A1

TITLE: System and method for pre-processing input data to a non-linear model for use in electronic commerce

PUBLICATION-DATE: July 24, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ferguson, Bruce	Round Rock	TX	US	
Hartman, Eric	Austin	TX	US	

US-CL-CURRENT: 706/21; 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Drawn D.
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 3. Document ID: US 20030065631 A1

L2: Entry 3 of 34

File: PGPB

Apr 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030065631

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030065631 A1

TITLE: Pipelined hardware implementation of a neural network circuit

PUBLICATION-DATE: April 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
McBride, Chad B.	Rochester	MN	US	

US-CL-CURRENT: 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Drawn D.
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 4. Document ID: US 20030061184 A1

L2: Entry 4 of 34

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061184

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030061184 A1

TITLE: Method and a system for calculating the values of the neurons of a neural network

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Masgonty, Jean-Marc	Marin		CH	
Vuilleumier, Philippe	Yverdon		CH	
Masa, Peter	Les Hauts-Geneveys		CH	
Piguet, Christian	Neuchatel		CH	

US-CL-CURRENT: 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Drawn D.
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 5. Document ID: US 20020133243 A1

L2: Entry 5 of 34

File: PGPB

Sep 19, 2002

PGPUB-DOCUMENT-NUMBER: 20020133243
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020133243 A1

TITLE: Non-integer order dynamic systems

PUBLICATION-DATE: September 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Abbisso, Salvatore	Augusta		IT	
Caponetto, Riccardo	Catania		IT	
Diamante, Olga	Siracusa		IT	
Porto, Domenico	Pedara		IT	
Di Cola, Eusebio	Messina		IT	
Fortuna, Luigi	Siracusa		IT	

US-CL-CURRENT: 700/42; 706/1, 706/15

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINIC](#) | [Drawings](#)

6. Document ID: US 20020073053 A1

L2: Entry 6 of 34

File: PGPB

Jun 13, 2002

PGPUB-DOCUMENT-NUMBER: 20020073053
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020073053 A1

TITLE: Method and circuits for associating a complex operator to each component of an input pattern presented to an artificial neural network

PUBLICATION-DATE: June 13, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Tremiolles, Ghislain Imbert de	Saint Paul		FR	
Tannhof, Pascal	Fontainebleau		FR	

US-CL-CURRENT: 706/26; 706/15

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINIC](#) | [Drawings](#)

7. Document ID: US 20020059150 A1

L2: Entry 7 of 34

File: PGPB

May 16, 2002

PGPUB-DOCUMENT-NUMBER: 20020059150
PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020059150 A1

TITLE: Method and circuits for associating a norm to each component of an input pattern presented to a neural network

PUBLICATION-DATE: May 16, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
de Tremiolles, Ghislain Imbert	Saint-Paul		FR	
Tannhof, Pascal	Fontainebleau		FR	

US-CL-CURRENT: 706/15

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn](#)

8. Document ID: US 6879971 B1

L2: Entry 8 of 34

File: USPT

Apr 12, 2005

US-PAT-NO: 6879971

DOCUMENT-IDENTIFIER: US 6879971 B1

TITLE: Automated method for building a model

DATE-ISSUED: April 12, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Keeler; James D.	Austin	TX		
Hartman; Eric J.	Austin	TX		
Godbole; Devendra B.	Austin	TX		
Piche; Steve	Austin	TX		
Arbila; Laura	Austin	TX		
Ellinger; Joshua	Austin	TX		
Ferguson, II; R. Bruce	Round Rock	TX		
Krauskopf; John	Austin	TX		
Kempf; Jill L.	Austin	TX		
O'Hara; Steven A.	Round Rock	TX		
Strauss; Audrey	Austin	TX		
Telang; Jitendra W.	Austin	TX		

US-CL-CURRENT: 706/21; 706/15, 706/23, 706/903, 706/906, 706/907

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn](#)

9. Document ID: US 6801655 B2

L2: Entry 9 of 34

File: USPT

Oct 5, 2004

US-PAT-NO: 6801655
DOCUMENT-IDENTIFIER: US 6801655 B2

TITLE: Spatial image processor

DATE-ISSUED: October 5, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Woodall; Roger L.	Jewett City	CT		

US-CL-CURRENT: 382/156; 382/305, 701/59, 704/232, 706/15, 706/42

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KMC](#) | [Drawn](#)

10. Document ID: US 6782373 B2

L2: Entry 10 of 34

File: USPT

Aug 24, 2004

US-PAT-NO: 6782373

DOCUMENT-IDENTIFIER: US 6782373 B2

TITLE: Method and circuits for associating a norm to each component of an input pattern presented to a neural network

DATE-ISSUED: August 24, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
de Tremiolles; Ghislain Imbert	Saint-Paul			FR
Tannhof; Pascal	Fontainebleau			FR

US-CL-CURRENT: 706/15; 706/26, 706/27

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KMC](#) | [Drawn](#)

11. Document ID: US 6768515 B1

L2: Entry 11 of 34

File: USPT

Jul 27, 2004

US-PAT-NO: 6768515

DOCUMENT-IDENTIFIER: US 6768515 B1

** See image for Certificate of Correction **

TITLE: Two architectures for integrated realization of sensing and processing in a single device

DATE-ISSUED: July 27, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
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Erten; Gamze	Okemos	MI
Salam; Fathi M.	Okemos	MI

US-CL-CURRENT: 348/308; 348/294, 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	De
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 12. Document ID: US 6708160 B1

L2: Entry 12 of 34

File: USPT

Mar 16, 2004

US-PAT-NO: 6708160

DOCUMENT-IDENTIFIER: US 6708160 B1

TITLE: Object nets

DATE-ISSUED: March 16, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Werbos; Paul J.	Arlington	VA	22203	

US-CL-CURRENT: 706/30; 706/15, 706/23

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	De
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 13. Document ID: US 6650779 B2

L2: Entry 13 of 34

File: USPT

Nov 18, 2003

US-PAT-NO: 6650779

DOCUMENT-IDENTIFIER: US 6650779 B2

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for analyzing an image to detect and identify patterns

DATE-ISSUED: November 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Vachtesvanos; George J.	Marietta	GA		
Dorrity; Lewis J.	Marietta	GA		
Wang; Peng	Atlanta	GA		
Echauz; Javier	Mayaguez	PR		
Mufti; Muid	Rawalpindi Cantt			PK

US-CL-CURRENT: 382/228; 348/88, 348/92, 382/195, 382/218, 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	De
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□ 14. Document ID: US 6577960 B1

L2: Entry 14 of 34

File: USPT

Jun 10, 2003

US-PAT-NO: 6577960

DOCUMENT-IDENTIFIER: US 6577960 B1

** See image for Certificate of Correction **TITLE: Liquid gauging apparatus using a time delay neural network

DATE-ISSUED: June 10, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rabelo; Luis C.	Eden Prairie	MN		
Walker; Mark	Mission Viejo	CA		
Zakrewski; Radoslaw R.	South Burlington	VT		

US-CL-CURRENT: 702/55; 706/15, 706/23

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOMC	Drazen D
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□ 15. Document ID: US 6523018 B1

L2: Entry 15 of 34

File: USPT

Feb 18, 2003

US-PAT-NO: 6523018

DOCUMENT-IDENTIFIER: US 6523018 B1

TITLE: Neural chip architecture and neural networks incorporated therein

DATE-ISSUED: February 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Louis; Didier	Fontainebleau			FR
Tannhof; Pascal	Cely en Biere			FR
Steimle; Andre	Evry			FR

US-CL-CURRENT: 706/27; 706/15, 706/26, 706/34, 711/5

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOMC	Drazen D
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□ 16. Document ID: US 6502083 B1

L2: Entry 16 of 34

File: USPT

Dec 31, 2002

US-PAT-NO: 6502083

DOCUMENT-IDENTIFIER: US 6502083 B1

TITLE: Neuron architecture having a dual structure and neural networks incorporating the same

DATE-ISSUED: December 31, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Louis; Didier	Fontainebleau			FR
Tannhof; Pascal	Cely-en-Biere			FR
Steimle; Andre	Evry			FR

US-CL-CURRENT: 706/27; 706/15, 706/26, 706/41, 706/42

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KWMC](#) | [Drawn](#)

17. Document ID: US 6404750 B1

L2: Entry 17 of 34

File: USPT

Jun 11, 2002

US-PAT-NO: 6404750

DOCUMENT-IDENTIFIER: US 6404750 B1

TITLE: Sensor-assisted aloha for wireless networks

DATE-ISSUED: June 11, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Wicker; Stephen B.	Ithaca	NY		
Fine; Terrence L.	Ithaca	NY		

US-CL-CURRENT: 370/329; 370/462, 455/456.1, 706/15, 706/40

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KWMC](#) | [Drawn](#)

18. Document ID: US 6347309 B1

L2: Entry 18 of 34

File: USPT

Feb 12, 2002

US-PAT-NO: 6347309

DOCUMENT-IDENTIFIER: US 6347309 B1

TITLE: Circuits and method for shaping the influence field of neurons and neural networks resulting therefrom

DATE-ISSUED: February 12, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
De Tremiolles; Ghislain Imbert	Paris			FR

Tannhof; Pascal

Fontainebleav

FR

US-CL-CURRENT: 706/20; 706/15, 706/18

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	Op
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□ 19. Document ID: US 6278799 B1

L2: Entry 19 of 34

File: USPT

Aug 21, 2001

US-PAT-NO: 6278799

DOCUMENT-IDENTIFIER: US 6278799 B1

TITLE: Hierarchical data matrix pattern recognition system

DATE-ISSUED: August 21, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Hoffman; Efrem H.	Winnipeg, Manitoba			CA

US-CL-CURRENT: 382/159; 382/155, 382/156, 382/157, 706/15, 706/20

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	Op
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□ 20. Document ID: US 6259824 B1

L2: Entry 20 of 34

File: USPT

Jul 10, 2001

US-PAT-NO: 6259824

DOCUMENT-IDENTIFIER: US 6259824 B1

TITLE: Image processing apparatus utilizing a neural network to improve printed image quality

DATE-ISSUED: July 10, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Sekiya; Toshiyuki	Yokohama			JP

US-CL-CURRENT: 382/274; 382/156, 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Drawn	Op
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□ 21. Document ID: US 6185554 B1

L2: Entry 21 of 34

File: USPT

Feb 6, 2001

US-PAT-NO: 6185554

DOCUMENT-IDENTIFIER: US 6185554 B1

TITLE: Methods for searching a knowledge base

DATE-ISSUED: February 6, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Bennett; Victor A.	Rockwall County	TX		

US-CL-CURRENT: 707/3; 370/911, 706/15, 707/2

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KRC](#) | [Draw](#)

22. Document ID: US 6169981 B1

L2: Entry 22 of 34

File: USPT

Jan 2, 2001

US-PAT-NO: 6169981

DOCUMENT-IDENTIFIER: US 6169981 B1

TITLE: 3-brain architecture for an intelligent decision and control system

DATE-ISSUED: January 2, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Werbos; Paul J.	College Park	MD	20740-2403	

US-CL-CURRENT: 706/23; 706/15, 706/16, 706/26, 706/27

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KRC](#) | [Draw](#)

23. Document ID: US 6092058 A

L2: Entry 23 of 34

File: USPT

Jul 18, 2000

US-PAT-NO: 6092058

DOCUMENT-IDENTIFIER: US 6092058 A

TITLE: Automatic aiding of human cognitive functions with computerized displays

DATE-ISSUED: July 18, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Smyth; Christopher C.	Fallston	MD		

US-CL-CURRENT: 706/10; 600/554, 706/15, 706/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOOC	Drawn Ds
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24. Document ID: US 5999922 A

L2: Entry 24 of 34

File: USPT

Dec 7, 1999

US-PAT-NO: 5999922

DOCUMENT-IDENTIFIER: US 5999922 A

** See image for Certificate of Correction **

TITLE: Neuroprocessing service

DATE-ISSUED: December 7, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Tanaka; Takehiko	Kawasaki			JP
Yokono; Masayuki	Kawasaki			JP

US-CL-CURRENT: 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOOC	Drawn Ds
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25. Document ID: US 5854993 A

L2: Entry 25 of 34

File: USPT

Dec 29, 1998

US-PAT-NO: 5854993

DOCUMENT-IDENTIFIER: US 5854993 A

TITLE: Component machine testing using neural network processed vibration data analysis

DATE-ISSUED: December 29, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Grichnik; Anthony J.	Peoria	IL		

US-CL-CURRENT: 702/54; 706/15, 706/20, 706/912, 73/581

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOOC	Drawn Ds
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26. Document ID: US 5832469 A

L2: Entry 26 of 34

File: USPT

Nov 3, 1998

US-PAT-NO: 5832469

DOCUMENT-IDENTIFIER: US 5832469 A

TITLE: Electronic system organized as a matrix network of functional cells

DATE-ISSUED: November 3, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Marchal; Pierre	Valangin			CH
Piguet; Christian	Neuchatel			CH
Nussbaum; Pascal	Cortaillod			CH

US-CL-CURRENT: 706/15; 706/16

Full | Title | Citation | Front | Review | Classification | Date | Reference | | Claims | KINIC | Drawn D

27. Document ID: US 5812993 A

L2: Entry 27 of 34

File: USPT

Sep 22, 1998

US-PAT-NO: 5812993

DOCUMENT-IDENTIFIER: US 5812993 A

TITLE: Digital hardware architecture for realizing neural network

DATE-ISSUED: September 22, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ginosar; Ran	Nofit			IL
Weinberg; Nitzan	Haifa			IL

US-CL-CURRENT: 706/26; 706/15, 706/25, 706/27, 706/41, 706/43

Full | Title | Citation | Front | Review | Classification | Date | Reference | | Claims | KINIC | Drawn D

28. Document ID: US 5758027 A

L2: Entry 28 of 34

File: USPT

May 26, 1998

US-PAT-NO: 5758027

DOCUMENT-IDENTIFIER: US 5758027 A

TITLE: Apparatus and method for measuring the fidelity of a system

DATE-ISSUED: May 26, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE ZIP CODE	COUNTRY
Meyers; Martin Howard	Montclair	NJ	
Tarraf; Ahmed A.	Bayonne	NJ	
Weaver; Carl Francis	Hanover Township, Morris County	NJ	

US-CL-CURRENT: 706/26; 704/259, 706/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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□ 29. Document ID: US 5717832 A

L2: Entry 29 of 34

File: USPT

Feb 10, 1998

US-PAT-NO: 5717832

DOCUMENT-IDENTIFIER: US 5717832 A

** See image for Certificate of Correction **

TITLE: Neural semiconductor chip and neural networks incorporated therein

DATE-ISSUED: February 10, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Steimle; Andre	Evry			FR
Tannhof; Pascal	Cely En Biere			FR
Paillet; Guy	Montpellier			FR

US-CL-CURRENT: 706/33; 706/15, 706/41

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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□ 30. Document ID: US 5550951 A

L2: Entry 30 of 34

File: USPT

Aug 27, 1996

US-PAT-NO: 5550951

DOCUMENT-IDENTIFIER: US 5550951 A

TITLE: Metrics for specifying and/or testing neural networks

DATE-ISSUED: August 27, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Woodall; Roger L.	Jewett City	CT		

US-CL-CURRENT: 706/15; 702/108, 706/31

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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□ 31. Document ID: US 5517597 A

L2: Entry 31 of 34

File: USPT

May 14, 1996

US-PAT-NO: 5517597

DOCUMENT-IDENTIFIER: US 5517597 A

TITLE: Convolutional expert neural system (ConExNS)

DATE-ISSUED: May 14, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Aparicio, IV; Manuel	Arlington	TX		
Otto; Samuel E.	Grapevine	TX		

US-CL-CURRENT: 706/26; 706/15, 706/25[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | [Claims](#) | [KMD](#) | [Drawn](#)

32. Document ID: US 5438293 A

L2: Entry 32 of 34

File: USPT

Aug 1, 1995

US-PAT-NO: 5438293

DOCUMENT-IDENTIFIER: US 5438293 A

TITLE: Low power analog absolute differencing circuit and architecture

DATE-ISSUED: August 1, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Guerrieri; Roberto	Bologna			IT
Kramer; Alan	Berkeley	CA		

US-CL-CURRENT: 327/355; 327/361, 706/15, 706/38[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | [Claims](#) | [KMD](#) | [Drawn](#)

33. Document ID: US 5222193 A

L2: Entry 33 of 34

File: USPT

Jun 22, 1993

US-PAT-NO: 5222193

DOCUMENT-IDENTIFIER: US 5222193 A

TITLE: Training system for neural networks and the like

DATE-ISSUED: June 22, 1993

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Brooks; William O.	Aloha		OR	

Cossitt; Alan J.	Portland	OR
Helm; Richard K.	Aloha	OR
Johnson; Louis J.	Beaverton	OR
McNamee; Raymond E.	Boring	OR

US-CL-CURRENT: 706/25; 706/15, 706/26

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KMC](#) | [Drawn](#)

34. Document ID: US 4804250 A

L2: Entry 34 of 34

File: USPT

Feb 14, 1989

US-PAT-NO: 4804250

DOCUMENT-IDENTIFIER: US 4804250 A

** See image for Certificate of Correction **

TITLE: Optical processor for an adaptive pattern classifier

DATE-ISSUED: February 14, 1989

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Johnson; John L.	Huntsville	AL		

US-CL-CURRENT: 349/17; 706/15, 706/40, 708/816

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KMC](#) | [Drawn](#)

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[Print](#)

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[Bkwd Refs](#)

[Generate OACS](#)

Terms

Documents

L1 and multiplex\$4 and neural near network

34

Display Format: [Change Format](#)

[Previous Page](#)

[Next Page](#)

[Go to Doc#](#)

Refine Search

Search Results -

Terms	Documents
multiplex\$4 and neural near network and arithmetic and logic and shifter	18

Database:

US Pre-Grant Publication Full-Text Database
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 IBM Technical Disclosure Bulletins

Search:

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<input type="button" value="Recall Text"/>	<input type="button" value="Clear"/>	<input type="button" value="Interrupt"/>

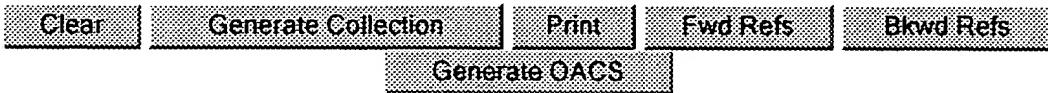
Search History

DATE: Thursday, April 21, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set</u>
				<u>Name</u>
	side by side			result set
DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR				
L3	multiplex\$4 and neural near network and arithmetic and logic and shifter		18	L3
L2	L1 and multiplex\$4 and neural near network		34	L2
L1	706/15.ccls.		316	L1

END OF SEARCH HISTORY

Hit List



Search Results - Record(s) 1 through 18 of 18 returned.

1. Document ID: US 20020059152 A1

Using default format because multiple data bases are involved.

L3: Entry 1 of 18

File: PGPB

May 16, 2002

PGPUB-DOCUMENT-NUMBER: 20020059152

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020059152 A1

TITLE: NEURAL PROCESSING MODULE WITH INPUT ARCHITECTURES THAT MAKE MAXIMAL USE OF A WEIGHTED SYNAPSE ARRAY

PUBLICATION-DATE: May 16, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
CARSON, JOHN C.	CORONA DEL MAR	CA	US	
SAUNDERS, CHRIST H.	LAGUNA NIGUEL	CA	US	

US-CL-CURRENT: 706/20



2. Document ID: US 6765958 B1

L3: Entry 2 of 18

File: USPT

Jul 20, 2004

US-PAT-NO: 6765958

DOCUMENT-IDENTIFIER: US 6765958 B1

TITLE: High-speed adaptive interconnect architecture

DATE-ISSUED: July 20, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dowling; Eric Morgan	Holmes Beach	FL	34217	

US-CL-CURRENT: 375/232; 375/229, 375/230



3. Document ID: US 6567834 B1

L3: Entry 3 of 18

File: USPT

May 20, 2003

US-PAT-NO: 6567834

DOCUMENT-IDENTIFIER: US 6567834 B1

**** See image for Certificate of Correction ****

TITLE: Implementation of multipliers in programmable arrays

DATE-ISSUED: May 20, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Marshall; Alan David	Bristol			GB
Stansfield; Anthony	Bristol			GB
Vuillemin; Jean	Paris			FR

US-CL-CURRENT: 708/626; 708/627, 708/630 Full | Title | Citation | Front | Review | Classification | Date | Reference | | Claims | KOMC | Drawn D. 4. Document ID: US 6389404 B1

L3: Entry 4 of 18

File: USPT

May 14, 2002

US-PAT-NO: 6389404

DOCUMENT-IDENTIFIER: US 6389404 B1

TITLE: Neural processing module with input architectures that make maximal use of a weighted synapse array

DATE-ISSUED: May 14, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Carson; John C.	Corona Del Mar	CA		
Saunders; Christ H.	Laguna Niguel	CA		

US-CL-CURRENT: 706/18 Full | Title | Citation | Front | Review | Classification | Date | Reference | | Claims | KOMC | Drawn D. 5. Document ID: US 5898603 A

L3: Entry 5 of 18

File: USPT

Apr 27, 1999

US-PAT-NO: 5898603

DOCUMENT-IDENTIFIER: US 5898603 A

TITLE: Method and apparatus for approximating a sigmoidal response using digital circuitry

DATE-ISSUED: April 27, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Osborn; Stephen G.	Austin	TX		

US-CL-CURRENT: 708/270

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) |  | [Claims](#) | [KINIC](#) | [Download](#)

6. Document ID: US 5892962 A

L3: Entry 6 of 18

File: USPT

Apr 6, 1999

US-PAT-NO: 5892962

DOCUMENT-IDENTIFIER: US 5892962 A

TITLE: FPGA-based processor

DATE-ISSUED: April 6, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Cloutier; Jocelyn	North Brunswick	NJ		

US-CL-CURRENT: 712/16; 712/10, 712/11, 712/22

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) |  | [Claims](#) | [KINIC](#) | [Download](#)

7. Document ID: US 5870138 A

L3: Entry 7 of 18

File: USPT

Feb 9, 1999

US-PAT-NO: 5870138

DOCUMENT-IDENTIFIER: US 5870138 A

**** See image for Certificate of Correction ****

TITLE: Facial image processing

DATE-ISSUED: February 9, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Smith; Anthony	Dublin			IE
Sako; Hiroshi	Dublin			IE
Sutherland; Alistair	Dublin			IE
Abe; Masahiro	Dublin			IE

US-CL-CURRENT: 348/143; 348/169, 348/578

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Dra	De
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8. Document ID: US 5621815 A

L3: Entry 8 of 18

File: USPT

Apr 15, 1997

US-PAT-NO: 5621815

DOCUMENT-IDENTIFIER: US 5621815 A

TITLE: Global threshold method and apparatus

DATE-ISSUED: April 15, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Talukdar; Dipankar	Buffalo	NY		
Sridhar; Ramalingam	East Amherst	NY		
Demjanenko; Victor	North Tonawanda	NY		

US-CL-CURRENT: 382/159; 358/466, 382/172, 382/270, 382/271

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Dra	De
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9. Document ID: US 5535148 A

L3: Entry 9 of 18

File: USPT

Jul 9, 1996

US-PAT-NO: 5535148

DOCUMENT-IDENTIFIER: US 5535148 A

TITLE: Method and apparatus for approximating a sigmoidal response using digital circuitry

DATE-ISSUED: July 9, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Osborn; Stephen G.	Austin	TX		
Goke; L. Rodney	Austin	TX		

US-CL-CURRENT: 708/250; 708/277

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMNC	Dra	De
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10. Document ID: US 5506797 A

L3: Entry 10 of 18

File: USPT

Apr 9, 1996

US-PAT-NO: 5506797

DOCUMENT-IDENTIFIER: US 5506797 A

TITLE: Nonlinear function generator having efficient nonlinear conversion table and format converter

DATE-ISSUED: April 9, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Koshiba; Yuichi	Hyogo			JP

US-CL-CURRENT: 708/272; 708/204

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | | [Claims](#) | [KDDC](#) | [Drawn D](#)

11. Document ID: US 5487153 A

L3: Entry 11 of 18

File: USPT

Jan 23, 1996

US-PAT-NO: 5487153

DOCUMENT-IDENTIFIER: US 5487153 A

TITLE: Neural network sequencer and interface apparatus

DATE-ISSUED: January 23, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Hammerstrom; Daniel W.	Aloha	OR		
Mueller; Dean W.	Portland	OR		
Owens; Stephen G.	Portland	OR		

US-CL-CURRENT: 718/100

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | | | | [Claims](#) | [KDDC](#) | [Drawn D](#)

12. Document ID: US 5402369 A

L3: Entry 12 of 18

File: USPT

Mar 28, 1995

US-PAT-NO: 5402369

DOCUMENT-IDENTIFIER: US 5402369 A

TITLE: Method and apparatus for digital multiplication based on sums and differences of finite sets of powers of two

DATE-ISSUED: March 28, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Main; David R.	Boulder Creek	CA		

US-CL-CURRENT: 708/625; 708/620

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMC	Drawn
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13. Document ID: US 5263122 A

L3: Entry 13 of 18

File: USPT

Nov 16, 1993

US-PAT-NO: 5263122

DOCUMENT-IDENTIFIER: US 5263122 A

TITLE: Neural network architecture

DATE-ISSUED: November 16, 1993

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Nunally; Patrick O.	Diamond Bar	CA		

US-CL-CURRENT: 706/41

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMC	Drawn
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14. Document ID: US 5165010 A

L3: Entry 14 of 18

File: USPT

Nov 17, 1992

US-PAT-NO: 5165010

DOCUMENT-IDENTIFIER: US 5165010 A

TITLE: Information processing system

DATE-ISSUED: November 17, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Masuda; Noboru	Kokubunji			JP
Yasunaga; Moritoshi	Kawaguchi			JP
Yamada; Minoru	Hanno			JP
Masaki; Akira	Musashino			JP
Asai; Mitsuo	Kokubunji			JP
Hirai; Yuzo	Tsukuba			JP
Yagyu; Masayoshi	Kokubunji			JP
Hayashi; Takehisa	Kodaira			JP
Doi; Toshio	Kokubunji			JP
Ishibashi; Kenichi	Kokubunji			JP

US-CL-CURRENT: 706/42

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMPC	Drawn Ds
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□ 15. Document ID: US 5138695 A

L3: Entry 15 of 18

File: USPT

Aug 11, 1992

US-PAT-NO: 5138695

DOCUMENT-IDENTIFIER: US 5138695 A

**** See image for Certificate of Correction ****

TITLE: Systolic array image processing system

DATE-ISSUED: August 11, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Means; Robert W.	Rancho Santa Fe	CA		
Sklar; Horace J.	Escondido	CA		

US-CL-CURRENT: 706/41; 382/156, 382/278, 706/22

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMPC	Drawn Ds
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□ 16. Document ID: US 5121498 A

L3: Entry 16 of 18

File: USPT

Jun 9, 1992

US-PAT-NO: 5121498

DOCUMENT-IDENTIFIER: US 5121498 A

TITLE: Translator for translating source code for selective unrolling of loops in the source code

DATE-ISSUED: June 9, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Gilbert; Ira H.	Carlisle	MA		
Ciccia; Nicodemo A.	North Reading	MA		

US-CL-CURRENT: 717/149; 717/141

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMPC	Drawn Ds
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□ 17. Document ID: US 5038282 A

L3: Entry 17 of 18

File: USPT

Aug 6, 1991

US-PAT-NO: 5038282

DOCUMENT-IDENTIFIER: US 5038282 A

TITLE: Synchronous processor with simultaneous instruction processing and data transfer

DATE-ISSUED: August 6, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Gilbert; Ira H.	Carlisle	MA		
Ciccia; Nicodemo A.	North Reading	MA		

US-CL-CURRENT: 712/14[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)**18. Document ID: US 5014235 A**

L3: Entry 18 of 18

File: USPT

May 7, 1991

US-PAT-NO: 5014235

DOCUMENT-IDENTIFIER: US 5014235 A

TITLE: Convolution memory

DATE-ISSUED: May 7, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Morton; Steven G.	Oxford	CT	06483	

US-CL-CURRENT: 708/520; 365/185.11, 708/607[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KUMC](#) | [Drawn D](#)[Clear](#)[Generate Collection](#)[Print](#)[Fwd Refs](#)[Bkwd Refs](#)[Generate OACS](#)

Terms

Documents

multiplex\$4 and neural near network and arithmetic and logic and shifter

18

Display Format: [Change Format](#)[Previous Page](#)[Next Page](#)[Go to Doc#](#)

Refine Search

Search Results -

Terms	Documents
L2 and buffer and input and output	11

Database:

US Pre-Grant Publication Full-Text Database
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Search:

L3

Search History

DATE: Tuesday, May 24, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query
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Hit Count Set Name
result set

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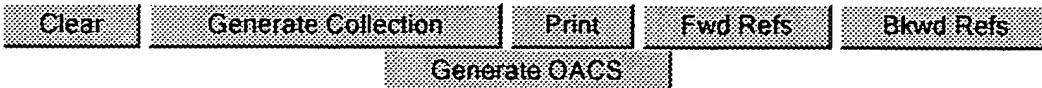
L3 L2 and buffer and input and output 11 L3

L2 programmable near memory and neural near network 92 L2

L1 5,165,010.pn. 2 L1

END OF SEARCH HISTORY

Hit List



Search Results - Record(s) 1 through 11 of 11 returned.

1. Document ID: US 20050005266 A1

L3: Entry 1 of 11

File: PGPB

Jan 6, 2005

PGPUB-DOCUMENT-NUMBER: 20050005266
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050005266 A1

TITLE: Method of and apparatus for realizing synthetic knowledge processes in devices for useful applications

PUBLICATION-DATE: January 6, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Datig, William E.	Dix Hills	NY	US	

US-CL-CURRENT: 717/136; 715/523



2. Document ID: US 20030123706 A1

L3: Entry 2 of 11

File: PGPB

Jul 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030123706
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030123706 A1

TITLE: System for controlling exterior vehicle lights

PUBLICATION-DATE: July 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Stam, Joseph S.	Holland	MI	US	
Bechtel, Jon H.	Holland	MI	US	
Reese, Spencer D.	Fort Wayne	IN	US	
Roberts, John K.	East Grand Rapids	MI	US	
Tonar, William L.	Holland	MI	US	
Poe, G. Bruce	Hamilton	MI	US	
Newhouse, Douglas J.	Zeeland	MI	US	

US-CL-CURRENT: 382/104[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINDC](#) | [Drawn Ds](#)**3. Document ID: US 20030123705 A1**

L3: Entry 3 of 11

File: PGPB

Jul 3, 2003

PGPUB-DOCUMENT-NUMBER: 20030123705

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030123705 A1

TITLE: System for controlling exterior vehicle lights

PUBLICATION-DATE: July 3, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Stam, Joseph S.	Holland	MI	US	
Bechtel, Jon H.	Holland	MI	US	
Reese, Spencer D.	Fort Wayne	IN	US	
Roberts, John K.	East Grand Rapids	MI	US	
Tonar, William L.	Holland	MI	US	
Poe, G. Bruce	Hamilton	MI	US	
Newhouse, Douglas J.	Zeeland	MI	US	

US-CL-CURRENT: 382/104[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KINDC](#) | [Drawn Ds](#)**4. Document ID: US 6587573 B1**

L3: Entry 4 of 11

File: USPT

Jul 1, 2003

US-PAT-NO: 6587573

DOCUMENT-IDENTIFIER: US 6587573 B1

**** See image for Certificate of Correction ****

TITLE: System for controlling exterior vehicle lights

DATE-ISSUED: July 1, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Stam; Joseph S.	Holland	MI		
Bechtel; Jon H.	Holland	MI		
Reese; Spencer D.	Fort Wayne	IN		
Roberts; John K.	East Grand Rapids	MI		
Tonar; William L.	Holland	MI		

Poe; G. Bruce	Hamilton	MI
Newhouse; Douglas J.	Zeeland	MI

US-CL-CURRENT: 382/104; 340/930, 340/933

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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5. Document ID: US 6438737 B1

L3: Entry 5 of 11

File: USPT

Aug 20, 2002

US-PAT-NO: 6438737

DOCUMENT-IDENTIFIER: US 6438737 B1

**** See image for Certificate of Correction ****

TITLE: Reconfigurable logic for a computer

DATE-ISSUED: August 20, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Morelli; John	South Bend	IN		
Kendall; H. Richard	Mishawaka	IN		

US-CL-CURRENT: 716/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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6. Document ID: US 6340588 B1

L3: Entry 6 of 11

File: USPT

Jan 22, 2002

US-PAT-NO: 6340588

DOCUMENT-IDENTIFIER: US 6340588 B1

TITLE: Matrices with memories

DATE-ISSUED: January 22, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Nova; Michael P.	Rancho Santa Fe	CA		
Potash; Hanan	Austin	TX		

US-CL-CURRENT: 435/287.1; 435/287.2, 435/288.1, 435/288.3, 435/288.4, 435/288.7,
530/300, 530/334, 530/350, 536/23.1, 536/24.3, 536/25.3

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Drawn	De
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7. Document ID: US 6329139 B1

L3: Entry 7 of 11

File: USPT

Dec 11, 2001

US-PAT-NO: 6329139

DOCUMENT-IDENTIFIER: US 6329139 B1

TITLE: Automated sorting system for matrices with memory

DATE-ISSUED: December 11, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE ZIP CODE	COUNTRY
Nova; Michael P.	Rancho Santa Fe	CA	
Lillig; John E.	Alamo	CA	
Karunaratne; Kanchana Sanjaya Gunesekera	San Diego	CA	
Ewing; William	San Diego	CA	
Satoda; Yozo	San Diego	CA	
Potash; Hanan	Austin	TX	

US-CL-CURRENT: 435/6; 209/597, 209/604, 702/19, 702/20

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw	De
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 8. Document ID: US 5937202 A

L3: Entry 8 of 11

File: USPT

Aug 10, 1999

US-PAT-NO: 5937202

DOCUMENT-IDENTIFIER: US 5937202 A

TITLE: High-speed, parallel, processor architecture for front-end electronics, based on a single type of ASIC, and method use thereof

DATE-ISSUED: August 10, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Crosetto; Dario B.	DeSoto	TX		

US-CL-CURRENT: 712/19; 712/11

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw	De
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 9. Document ID: US 5465375 A

L3: Entry 9 of 11

File: USPT

Nov 7, 1995

US-PAT-NO: 5465375

DOCUMENT-IDENTIFIER: US 5465375 A

TITLE: Multiprocessor system with cascaded modules combining processors through a programmable logic cell array

DATE-ISSUED: November 7, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Thepaut; Andre	Plouzane			FR
Ouvradou; Gerald	Plouzane			FR

US-CL-CURRENT: 712/15; 700/8, 712/21, 712/22

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KWMC](#) | [Drawn Obj](#)

10. Document ID: US 5053638 A

L3: Entry 10 of 11

File: USPT

Oct 1, 1991

US-PAT-NO: 5053638

DOCUMENT-IDENTIFIER: US 5053638 A

TITLE: Semiconductor neural circuit device having capacitive coupling and operating method thereof

DATE-ISSUED: October 1, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Furutani; Kiyohiro	Hyogo			JP
Mashiko; Koichiro	Hyogo			JP

US-CL-CURRENT: 365/185.03; 365/185.1, 706/33, 708/801

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Claims](#) | [KWMC](#) | [Drawn Obj](#)

11. Document ID: US 4999525 A

L3: Entry 11 of 11

File: USPT

Mar 12, 1991

US-PAT-NO: 4999525

DOCUMENT-IDENTIFIER: US 4999525 A

** See image for Certificate of Correction **

TITLE: Exclusive-or cell for pattern matching employing floating gate devices

DATE-ISSUED: March 12, 1991

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Park; Chin S.	Sunnyvale	CA		

Castro; Herman A.

Shingle Springs

CA

US-CL-CURRENT: 326/52; 706/33, 708/801

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOMC	Drawn	De
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Terms

Documents

L2 and buffer and input and output

11

Display Format: [-] [Change Format](#)

[Previous Page](#) [Next Page](#) [Go to Doc#](#)

Refine Search

Search Results -

Terms	Documents
L1 and neural near network and control near logic	19

Database:

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JPO Abstracts Database
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Search:

L2

Search History

DATE: Tuesday, May 31, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query
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		<u>Hit Count</u>	<u>Set Name</u>
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L1	"field programmable gate array"	13142	<u>L1</u>

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1. Document ID: US 20050049470 A1

L2: Entry 1 of 19

File: PGPB

Mar 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050049470
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050049470 A1

TITLE: Multi-domain motion estimation and plethysmographic recognition using fuzzy neural-nets

PUBLICATION-DATE: March 3, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Terry, Alvin Mark	Longmont	CO	US	

US-CL-CURRENT: 600/323; 128/925

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KINIC](#) [Drawn D](#)

2. Document ID: US 20050046584 A1

L2: Entry 2 of 19

File: PGPB

Mar 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050046584
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20050046584 A1

TITLE: Asset system control arrangement and method

PUBLICATION-DATE: March 3, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Breed, David S.	Boonton Township	NJ	US	

US-CL-CURRENT: 340/825.72; 280/735, 340/5.71, 455/420

[Full](#) [Title](#) [Citation](#) [Front](#) [Review](#) [Classification](#) [Date](#) [Reference](#) [Sequences](#) [Attachments](#) [Claims](#) [KINIC](#) [Drawn D](#)

3. Document ID: US 20040230358 A1

L2: Entry 3 of 19

File: PGPB

Nov 18, 2004

PGPUB-DOCUMENT-NUMBER: 20040230358
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040230358 A1

TITLE: Monitoring and automatic equipment control systems

PUBLICATION-DATE: November 18, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Stam, Joseph S.	Holland	MI	US	
Pierce, Mark W.	Grandville	MI	US	
Bechtel, Jon H.	Holland	MI	US	
Spence, William R.	Holland	MI	US	
Turnbull, Robert R.	Holland	MI	US	
Eid, El-Sayed	Holland	MI	US	

US-CL-CURRENT: 701/49; 701/36[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUUC](#) | [Drawn Obj](#) 4. Document ID: US 20040193789 A1

L2: Entry 4 of 19

File: PGPB

Sep 30, 2004

PGPUB-DOCUMENT-NUMBER: 20040193789
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040193789 A1

TITLE: Associative memory device and method based on wave propagation

PUBLICATION-DATE: September 30, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Rudolf, Paul	San Antonio	TX	US	

US-CL-CURRENT: 711/108; 365/49[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KUUC](#) | [Drawn Obj](#) 5. Document ID: US 20040158543 A1

L2: Entry 5 of 19

File: PGPB

Aug 12, 2004

PGPUB-DOCUMENT-NUMBER: 20040158543

PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040158543 A1

TITLE: Self-programmable chip

PUBLICATION-DATE: August 12, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Salam, Fathi M.	Okemos	MI	US	
Waheed, Khurram	East Lansing	MI	US	

US-CL-CURRENT: 706/26; 708/300

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn D.](#)

6. Document ID: US 20040051795 A1

L2: Entry 6 of 19

File: PGPB

Mar 18, 2004

PGPUB-DOCUMENT-NUMBER: 20040051795
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040051795 A1

TITLE: Visual device, interlocking counter, and image sensor

PUBLICATION-DATE: March 18, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Ajioka, Yoshiaki	Gamagori-shi		JP	

US-CL-CURRENT: 348/239; 348/222.1

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KOMC](#) | [Drawn D.](#)

7. Document ID: US 20030183871 A1

L2: Entry 7 of 19

File: PGPB

Oct 2, 2003

PGPUB-DOCUMENT-NUMBER: 20030183871
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20030183871 A1

TITLE: Floating-gate analog circuit

PUBLICATION-DATE: October 2, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
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Dugger, Jeffery Don	Atlanta	GA	US
Hall, Tyson S.	Cleveland	TN	US
Hasler, Paul	Atlanta	GA	US
Anderson, David V.	Alpharetta	GA	US
Smith, Paul D.	Marietta	GA	US
Kucic, Matthew Raymond	Austell	GA	US
Bandyopadhyay, Abhishek	Atlanta	GA	US

US-CL-CURRENT: 257/314

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

8. Document ID: US 20030105949 A1

L2: Entry 8 of 19

File: PGPB

Jun 5, 2003

PGPUB-DOCUMENT-NUMBER: 20030105949

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030105949 A1

TITLE: Apparatus, method, system and executable module for configuration and operation of adaptive integrated circuitry having fixed, application specific computational elements

PUBLICATION-DATE: June 5, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Master, Paul L.	Sunnyvale	CA	US	
Smith, Stephen J.	Los Gatos	CA	US	
Watson, John	Edgewood	WA	US	

US-CL-CURRENT: 713/100

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#) | [Claims](#) | [KMC](#) | [Drawn](#)

9. Document ID: US 20020069375 A1

L2: Entry 9 of 19

File: PGPB

Jun 6, 2002

PGPUB-DOCUMENT-NUMBER: 20020069375

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020069375 A1

TITLE: System, method, and article of manufacture for data transfer across clock domains

PUBLICATION-DATE: June 6, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Bowen, Matt	Littlemore		GB	

US-CL-CURRENT: 713/400

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn D
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10. Document ID: US 6898097 B2

L2: Entry 10 of 19

File: USPT

May 24, 2005

US-PAT-NO: 6898097

DOCUMENT-IDENTIFIER: US 6898097 B2

TITLE: Floating-gate analog circuit

DATE-ISSUED: May 24, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dugger; Jeffery Don	Atlanta	GA		
Hall; Tyson S.	Cleveland	TN		
Hasler; Paul	Atlanta	GA		
Anderson; David V.	Alpharetta	GA		
Smith; Paul D.	Marietta	GA		
Kucic; Matthew Raymond	Austell	GA		
Bandyopadhyay; Abhishek	Atlanta	GA		

US-CL-CURRENT: 365/45; 326/37, 326/39, 365/185.03, 365/185.11

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawn D
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11. Document ID: US 6463425 B2

L2: Entry 11 of 19

File: USPT

Oct 8, 2002

US-PAT-NO: 6463425

DOCUMENT-IDENTIFIER: US 6463425 B2

TITLE: Neural network assisted multi-spectral segmentation system

DATE-ISSUED: October 8, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Raz; Ryan S.	Toronto			CA

US-CL-CURRENT: 706/20; 382/128

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. D.
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12. Document ID: US 6442535 B1

L2: Entry 12 of 19

File: USPT

Aug 27, 2002

US-PAT-NO: 6442535

DOCUMENT-IDENTIFIER: US 6442535 B1

**** See image for Certificate of Correction ****

TITLE: Method and apparatus for implementing a low cost, intelligent controller for a switched reluctance machine

DATE-ISSUED: August 27, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Yifan; Tang	St. Louis	MO		

US-CL-CURRENT: 706/4; 310/165, 310/168

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. D.
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13. Document ID: US 6438737 B1

L2: Entry 13 of 19

File: USPT

Aug 20, 2002

US-PAT-NO: 6438737

DOCUMENT-IDENTIFIER: US 6438737 B1

**** See image for Certificate of Correction ****

TITLE: Reconfigurable logic for a computer

DATE-ISSUED: August 20, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Morelli; John	South Bend	IN		
Kendall; H. Richard	Mishawaka	IN		

US-CL-CURRENT: 716/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Draw. D.
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14. Document ID: US 6434687 B1

L2: Entry 14 of 19

File: USPT

Aug 13, 2002

US-PAT-NO: 6434687

DOCUMENT-IDENTIFIER: US 6434687 B1

TITLE: System and method for accelerating web site access and processing utilizing a computer system incorporating reconfigurable processors operating under a single operating system image

DATE-ISSUED: August 13, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Huppenthal; Jon M.	Colorado Springs	CO		

US-CL-CURRENT: 712/32; 709/203, 709/219, 715/501.1, 715/513

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) |  | [Claims](#) | [KUMC](#) | [Drawn 62](#)

15. Document ID: US 6353841 B1

L2: Entry 15 of 19

File: USPT

Mar 5, 2002

US-PAT-NO: 6353841

DOCUMENT-IDENTIFIER: US 6353841 B1

**** See image for Certificate of Correction ****

TITLE: Reconfigurable processor devices

DATE-ISSUED: March 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Marshall; Alan David	Bristol			GB
Stansfield; Anthony	Bristol			GB
Vuillemin; Jean	Paris			FR

US-CL-CURRENT: 708/232

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) |  | [Claims](#) | [KUMC](#) | [Drawn 62](#)

16. Document ID: US 6339819 B1

L2: Entry 16 of 19

File: USPT

Jan 15, 2002

US-PAT-NO: 6339819

DOCUMENT-IDENTIFIER: US 6339819 B1

**** See image for Certificate of Correction ****

TITLE: Multiprocessor with each processor element accessing operands in loaded input buffer and forwarding results to FIFO output buffer

DATE-ISSUED: January 15, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY

Huppenthal; Jon M.	Colorado Springs	CO
Leskar; Paul A.	Colorado Springs	CO

US-CL-CURRENT: 712/16; 326/39, 326/41, 712/37

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWMC	Drawn D.
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17. Document ID: US 6278906 B1

L2: Entry 17 of 19

File: USPT

Aug 21, 2001

US-PAT-NO: 6278906

DOCUMENT-IDENTIFIER: US 6278906 B1

** See image for Certificate of Correction **

TITLE: Uncalibrated dynamic mechanical system controller

DATE-ISSUED: August 21, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Piepmeyer; Jennelle Armstrong	Severna Park	GA		
Lipkin; Harvey	Decatur	GA		
McMurray; Gary Von	Atlanta	GA		

US-CL-CURRENT: 700/250; 700/245, 700/258, 700/263, 700/29, 700/31, 700/59, 700/63

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWMC	Drawn D.
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18. Document ID: US 6247110 B1

L2: Entry 18 of 19

File: USPT

Jun 12, 2001

US-PAT-NO: 6247110

DOCUMENT-IDENTIFIER: US 6247110 B1

TITLE: Multiprocessor computer architecture incorporating a plurality of memory algorithm processors in the memory subsystem

DATE-ISSUED: June 12, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Huppenthal; Jon M.	Colorado Springs	CO		
Leskar; Paul A.	Colorado Springs	CO		

US-CL-CURRENT: 712/15

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWMC	Drawn D.
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19. Document ID: US 6233646 B1

L2: Entry 19 of 19

File: USPT

May 15, 2001

US-PAT-NO: 6233646

DOCUMENT-IDENTIFIER: US 6233646 B1

TITLE: Memory interface controller

DATE-ISSUED: May 15, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Hahm; Jin Ho	Daejeon			KR

US-CL-CURRENT: 711/2; 711/156

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Terms	Documents
L1 and neural near network and control near logic	19

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